



Settings

SW2.1 - ON = Map 0C0000h-0C7FFFh to RAM
 SW2.2 - ON = Map 0C8000h-0CFFFFh to RAM
 SW2.3 - ON = Map 0D0000h-0D7FFFh to RAM
 SW2.4 - ON = Map 0D8000h-0DFFFFh to RAM
 SW2.5 - ON = Map 0E0000h-0E7FFFh to RAM
 SW2.6 - ON = Map 0E8000h-0EFFFFh to RAM
 SW2.7 - ON = 64 KIB ROM, starting from 0F0000h
 OFF = 128 KIB ROM, starting from 0E0000h
 SW2.8 - ON = Monochrome display (MDA)
 OFF = Color display (CGA)

SW3.1 - ON = 1 I/O Wait State
 SW3.2 - ON = 2 I/O Wait States
 SW3.3 - ON = 3 I/O Wait States
 SW4.4 - ON = 4 I/O Wait States

J2 - OFF = Turbo mode off
 1-2 = Turbo mode on
 2-3 = Software controlled (port 61h bit 2)

J3 - 1-2 = Use IRQ12 for PS/2 mouse
 2-3 = Connect IRQ12 to ISA bus

J4 - ON for CL-GD542x, otherwise OFF

Notes

- For NMOS 8284 (U1) replace C52 and C53 with 510 ohm resistors R8 and R9
- SW2.7 must be ON if SW2.5 or SW2.6 are ON
- One and only one of SW3 switches must be ON
- VGA BIOS usually mapped to 0C0000h-0C7FFFh make sure that SW2.1 is OFF when using VGA